REFRACTORY MATERIAL SIS JUNCTION STRUCTURES

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Abstract

Niobium based SIS junction structures for microwave receiving devices were investigated. The possibility of the mixing efficiency improving for Nb-Al₂O₃-Nb junctions with a negative resistance on I-V curve near the gap voltage has been demonstrated. Comparative microwave investigation of the parallel and series DC biased arrays shows the advantage of the first one. SIS junction capacitance has been compensated with microstrip or lumped elements at the signal frequency. Such compensated structures were investigated at frequencies 38 and 76 GHz both in waveguide and quasioptic systems.

Introduction

Superconductor - Insulator - Superconductor (SIS) junctions are one of the most promising elements for low noise microwave receiving devices. SIS junctions have extremely high nonlinearity of the quasiparticle current and low intrinsic noise approaching the quantum limit. The investigations of the SIS junctions and arrays based on the soft materials have demonstrated quantum mixing with high conversion efficiency^{1,2}. Using series SIS arrays simplifies the microwave matching and expands the dynamic range of the receiver³. However, SIS junctions and arrays based on the soft materials are not rigid enough, they are subjected to aging. The aim of the present work has been an experimental investigation of the SIS mixing structure based on refractory material niobium.

Nb based SIS junctions

Earlier it has been demostrated that Nb-Nb_xO_y-Pb+B1 junctions⁴ are suitable for quantum mixing⁵. However the specific capacitance C/A (A - junction area) of such junctions are higher than for soft material junctions; so the conversion efficiency is decreased. Therefore Nb junctions with Al interlayer⁶, having small specific capacitance and high nonlinearity of the quasiparticle IVC, have been also investigated⁷.

Nb-Al₂O₃-Nb junctions were prepared by oxidation at room temperature of the Al layer (d = 30 - 40 Å) sputtered immediately after RF etching of the bottom Nb electrode. Acounter Nb electrode was deposited without vacuum breaking on the room temperature substrate; its geometry was shaped by lift off.

Prepared in this manner junctions have normal state resistance $R_{\rm N}$ = 1 - 10 Ω for areas A = 5 - 50 μm^2 ; ratios $R_J/R_{\rm N}$ are 5 - 25, where R_J = resistance at V = 2 mV. Nb-Al_2O_3-Nb junctions remain stable at thermal cycling between helium and room temperature and two year aging.

The experimental comparison of the capacitance for two different types of the Nb based junctions is performed by determination of the resonant frequencies in

specialy prepared long Josephson junctions. The ratio of the specific capacitance for intrinsic Nb oxide and Al_2O_3 is estimated as 2.5 ± 0.5.

A typical IVC of the Nb-Al₂O₃-Nb junctions is shown on Fig. 1 by solid line. One can see the hysteretic "knee" at $V > V_g = 2\Delta/e$ and residual slope at $V \simeq V_g$. It was demostrated in microscopic theory⁸ that proximity

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Fig. 1. IVC of Nb-Al₂O₃-Nb junction at T = 4.2 K - solid line. Dashed lines show theoretical IVCs of SNINS junction for three values of ζ (1).

effect on the boundary normal metal - superconductor in SNINS junctions leads to changing of the electron distribution function and consequently leads to residual slope at $V \simeq V_g$, "knee" at $V > V_g$, and deflection of the $I_c(T)$ dependence from "usual" tunnel one. For quantitative analysis the parameter

$$\zeta = \frac{\sigma_{\rm N} \xi_{\rm S} d_{\rm N}}{\sigma_{\rm S} \xi_{\rm N}^2} \tag{1}$$

was introduced⁸; where σ_N , $\sigma_S =$ conductivity and ξ_N , $\xi_S =$ coherence length for normal metal and superconductor correspondingly; $d_N =$ thikness of the normal layer. Theoretical IVCs for SNINS junction with two normal layers at T/T_c = 0.6 are shown on Fig. 1 by dashed lines for different values $\zeta = 0.1$, 0.3, 0.5. It is seen the experimental IVC conforms with theoretical one at $\zeta = 0.3$; the same estimation for ζ is obtained from comparison with the theory⁸-the experimental decreasing of the V_g and I_c, the value of the hysteresis on "knee", the slope at V \simeq V_g. For Nb- Nb_xO_y-Pb+Bi junctions the theory coin-

For Nb- Nb_xO_y-Pb+Bi junctions the theory coincides with experiment better for one normal layer model SNIS junction at $\zeta = 0.1 - 0.2$. It should be noted the residual slope at $V \simeq V_g$ and "knee" for Nb-Al₂O₃-Nb junctions prepared at the same oxidation conditions by SNAP technique⁹ are reduced significally. It indicates that the main normal layer is formed during RF cleaning of the bottom Nb electrode before Al sputtering.

Microwave properties of the Nb based junctions

The IVC peculiarities of the Nb based junctions ("knee", for example) manifest themselves in the junction microwave properties.In Fig. 2 the autonomous and pumped IVCs ($f_{LO} = 72$ GHz) are shown by dashed and solid line, respectively. The quantum current steps are evidently visible on the pumped curve. The variation of the IVC with local oscillator power can be explained by existence of the two independent current step sets, with distance between steps $\Delta V = hf_{LO}/e = 300 \ \mu V$ in in both sets. This is caused by existence of the two regions⁷ on the autonomous IVC where sharp changing of the quasiparticle current growth at $V < V_g$; second - near "knee" at $V > V_o$.

Fig. 2. Autonomous (dashed line) and pumped (solid line) IVCs of Nb-Al₂O₃-Nb junction at f_{LO} = 72 GHz. In the insert, the hysteretic current step is shown in detail.

In the insert of Fig. 2 the hysteretic current step is shown. The hysteresis appeared in the narrow local oscillator power range only It istaused by hysteresis on the autonomous IVC near "knee". Existence of the region with large or negative differential resistance indicates a possibility to obtain a conversion gain greater than unity¹⁰. Therefore for Nb-Al₂O₃-Nb junctions the best conversion efficiency ($G^{max} = 0.2$) is realised at V > V_g.

The extremely high "knee" nonlinearity on IVC of the Nb based junctions gives the possibility to observe quantum mixing at low frequencies (hf/e < $\delta V_g; \delta V_g$ is a gap spreading).

For comparison with photon-assisted tunneling theory¹⁰ we used the Nb-Nb_xOy-Pb+Bi junctions where $\gamma = 2\pi f CR_N$ was about 10 mm wavelength. Therefore the impedance of the junction has large imaginary part remaining nearly constant at all bias voltages and we can calculate the conversion gain $G(V, \alpha)^{10}$ and

$$G(V,\alpha) = \left\{ \begin{array}{c} \frac{\partial I(V,\alpha)}{2\partial \alpha} \right\}^2 \cdot \frac{R_d(V,\alpha)R_{RF}(V,\alpha)4R_dZ_o}{(hf/e)^2 (R_d + Z_o)^2}, \quad (2)$$

where

$$I(V,\alpha) = \bigotimes_{n=1}^{\infty} J_n^2(\alpha) \cdot I(V + nhf/e)$$
(3)

is pumped IVC, V_{RF} = RF voltage, α = eV_{RF}/hf, I(V) = autonomous IVC, R_d = dV/dI = the IF impedance for $f_{IF}{<<}f$.

$$R_{RF}^{-1} = (2hf/e)^{-1} \sum_{n=-\infty}^{\infty} \{J_n(\alpha) + J_{n-1}(\alpha) \cdot J_{n+1}(\alpha)\} \cdot \{I(V + (n + 1)hf/e) - I(V + (n - 1)hf/e\}, (4)\}$$

where R_{RF} is impedance of the junction for signal frequency, $Z_0 = IF$ impedance of the external circuit. The theoretical conversion gain curve G(V) calculated using (2) - (4) is shown on Fig. 3 (curve 3). It can be seen that there is good agreement between experiment (solid line) and theory¹⁰. Note, that for Nb-Al₂O₃-Nb junctions with smaller capacitance ($\gamma \sim 1$) it is necessary to take into account voltage dependence of the junction impedance; so it requires numerical calculations for G(V).



Fig. 3. Pumped IVC of Nb-Nb_x ρ_y -Pb+Bi junction for $f_{LO} = 76$ GHz - dashed line. Experimental conversion gain dependence G(V) is shown by solid line (curve 2), calculated G(V) - curve 3.

SIS junction arrays

The most important advantage of the N SIS junction arrays is the increased dynamic range as N^2 . Moreover it appears that the noise properties of an array are no worse than those in an equivalent single junction². For effective mixing the equality of the dc bias voltage on all junctions is necessary so the normal resistance tolerance should be not very large ($\Delta R_N/R_N << hf/eV_g$); it means the nonuniformity of the junctions should not be more than 5 % for f = 38 GHz.

We have investigated the SIS arrays with dc parallel biasing of the junctions⁴ (See Fig. 4). All thirteen junctions in the array are connected by superconductor inductive loops (L = 10⁻⁹ - 10⁻⁸H) which provides the equal dc biasing voltage on the junctions. This loop represents alarge RF impedance, therefore RF currents flow directly through SIS junctions. Junction nonuniformity results in variation in the LO power only. For low intermediate frequencies, the junctions are connected in parallel; the equivalent circuit for the IF signal is shown on Fig. 5. Using equations (2) - (4) and the equivalent circuit (Fig. 5) it is possible to obtain the expression for the conversion gain of the ar-



Fig. 4. Photograph of 13 junctions array with parallel dc biasing.



Fig. 5. Equivalent circuit of N-junction array.

ray:

$$G(V,\alpha) = \{ \sum_{i=1}^{13} -\frac{\partial I_i}{2\partial \alpha} \}^2 \cdot \frac{Z_o}{(hf/e)(1 + \sum_{i=1}^{13} Z_o/R_d^i) \cdot \sum_{i=1}^{13} 1/R_{RF}^i}$$
(5)

For uniform spread of the junction normal resistances in the array and at $R_{N}^{max}/R_{N}^{min} = 2$ the estimation from (5) shows that conversion gain of the array with parallel biasing is decreased by 3 dB only. It is much better than for usual series array with the same nonuniformity.

Thus SIS arrays with parallel dc biasing allow at the same time:

i) Increase the tolerance for junction nonuniformity; ii)Provide matching at the signal frequency of the source and RF-array impedance $R_{\rm RF}^{\rm ar} \simeq NR_{\rm N}$ (2hf/eVg); iii) Provide the matching at the IF frequency of the

iii) Provide the matching at the IF frequency of the output-array impedance and input-IF-amplifier resistance. So, parallel arrays give us the possibility to optimize the RF and IF matching simultaneously by choosing the number of junctions N. If we know Z_s and Z_o we can calculate the optimal value N:

$$Z_{s}/Z_{o} = R_{RF}^{ar}/R_{IF}^{ar} = N^{2}(R_{N}/R_{d})2hf/eV_{g} = N^{2} \cdot const .$$
(6)

For comparison different types of arrays we have investigated experimentally single junction, parallel, and series dc-biased arrays fabricated on the same substrate. The junctions have areas about 60 μ m². The reduced sensitivity for both type of arrays differ less than 1 dB in spite of the difference of the normal state resistance for N^2 = 169. Thus the dc-bias inductive circuits provide efficient isolation for RF current. Quantum mixing has been obtained for single junctions and both types of the arrays at f = 38 GHz, but conversion photon peaks for series arrays were smaller than ones for parallel arrays and single junctions. Saturation measurements were performed by IF photon peak reduction. Normalized quantum conversion efficiency vs normalized signal power for a single junction and both type of the arrays is shown in Fig. 6. Signal power is normalized



Fig. 6. Normalized conversion efficiency vs signal power for three types of the SIS structures: o - single junction; Δ - array with dc parallel biasing; \Box - series array.



Fig. 7. Three-junction parallel dc-biased array with open-circuit microstrip stubs.

to $(hf/e)^2/R_N$ which is approximately equal to the IF outpost saturation power for single junction. Fig. 6 shows that 3 dB reduction of the conversion gain takes place at the same input signal power level for both array types. This level is N² times more than for single junction that agrees with data for series arrays².

Junction capacitance tuning out.

The large junction capacitance effectively shunts the mixer at harmonic frequencies but must be tuned out at the signal frequency. This is usually accomplished by an elaborate matching circuit which leads to narrow bandwidth¹¹ and reduction of the junction real part impedance $1/(1 + \gamma^2)^{12}$. For optimal junction capacitance $(1 \leq \gamma \leq 10)$ this reduction is significant. Optimization of SIS mixer requires tuning element external to the junction but close enough to minimize losses in signal circuits¹¹⁻¹³.

We have investigated two types of junction capacitance tuning-out circuit. Fig. 7 shows three-junction array with open-circuited superconducting microstrip stubs (length $l_s > \lambda/4$) which act as a parallel inductance across the junctions^{11,13}. The data shows that the microstrips in our case are perpendicular to current flow and can be applied to mixer blocks with reduced-height waveguider. Counter Pb+Bi microstrip electrode (width w = 10 µm) is separated from bottom Nb one by SiO layer (d = 3000 - 4500 Å). The soft material of the counter electrode provides the possibility of resonant frequency tuning by changing of the stub length. Fig. 8 shows the detector responce ratio for junctions with and without capacitance tuning out at two different stub lengthes. The solid curves show the theoretical ones calculated for case $Z_s > R_{RF} = const$:



Fig.8. Detector response frequency dependence of the junction with tuning out capacitance by open-circuited microstrip stub for two lengthes $l_1=600\,\mu\text{m}$ and $l_2=750\,\mu\text{m}$.



Fig.9. Photography of a two-junction cell with superconducting inductive loop for tuning out of the capacitance. The equavalent circuit of the cell is shown in insert.

$$R/R_{0} = |Z|^{2} \cdot (1+\gamma^{2})/R_{RF}^{2},$$

$$|Z|^{2} = \{(1/R_{RF})^{2} + [2\pi fC + 1/\rho \cdot tg(\beta l_{S})]^{2}\}^{-1},$$
(7)

where Z= stub and junction parallel circuit impedance, ρ = characteristic impedance of the microstrip line, β = $2\pi/\epsilon/\lambda_0$ = propagation constant 14 . The resonance curve shape provides a convenient method for determination of the system parameters. For example, at frequency where $l_{\rm S}$ = $\lambda/4$, there is shunting of the junction impedance and response is decreased to zero. For data represented on Fig. 8 the best fitting takes place at ρ = 8Ω, C =6pF, $R_{\rm RF}$ = 1.4 Ω , $\sqrt{\epsilon}$ = 3.9, agreeing well with another estimation of these parameters.

In the second case we have investigated the tuning out of the junction capacitance by lump inductance provided at the same time adc parallel biasing of the junctions in array. Fig. 9 shows the cell of two SIS junctions with inductance loop($L = 2 \ 10^{-10}$ H is selected to resonate junction capacitance at signal frequency). Such cells are most suitable for using in parallel dc biasing arrays.

Planar quasi-optical mixer

In recent years an increased interest in application of quasi-optical mixers and detectors at mm wavelength has been observed¹⁵⁻¹⁷. The advantages of such devices are very wide bandwidth without any additional tuning and avoidance of the polarization losses. These advantages of the quasi-optical devices become more



Fig. 10. Detector response frequency dependence of the junction with capacitance tuning out by inductive loop (curve 1) and without tuning (curve 2). Curve 3 - ratio of the responses with and without tuning.

important at the short mm wavelengthes.

A receiving structure combining SIS junctions with tuning out circuit and planar antenna has been elaborated. The capacitance tuning out of Nb-Nb_O -Pb+Bi junction is provided by thin film superconducting y_{loop} with width 20µm and area 1000µm². For dc biasing, the capacitance (C_=0.3 pF) is formed in the loop. The receiving structure is arranged across circular waveguide (diameter 16 mm) for RF signal (50 - 70GHz). The special absorber installed around the sample provides nondistorted microwave field in vicinity of the junction; so we can estimate input substrate power by measuring power in oversize waveguide. Absorbed RF SIS junction power is determined by the first quasiparticle step oscillation. meastring The antenna losses of input signal power is 6±3 dB, taking into account antenna (R =70 Ω) and RF SIS junction ($R_N = 20\Omega$) mismatching.

We also have investigated the influence of tuning out element in this structure. Fig.10 shows the detector response frequency dependence of the structure with (curve 1) and without (curve 2) tuning out element. Nonmonotonic frequency dependence is caused by external electrodynamic system. Ratio of curve 1 and 2 gives the advantage (curve 3) caused by the tuning out element.

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